IN THE DRAWINGS:

OK TO ENTER

Submitted herewith is a request for approval of corrected drawings, including a corrected Fig. 3. A typographic error has been corrected, in which the "<" between steps S7 and S8 is changed to a -->--. Corrections are annotated in red ink. Also attached is a formal Fig. 3 with the correction made thereto.

IN THE CLAIMS

WILL NOT BE ENTERED

Please amend claims 3-5 as follows:

3. (Twice Amended) The semiconductor integrated circuit of claim 2,

wherein said bias adjustment circuit is configured to step up said second bias current in response to judgment that a value obtained by said successively summing is larger than said reference value by said comparator circuit,

wherein said control circuit is configured to cease operation thereof in response to judgment that said value obtained by said successively summing is smaller than said reference value

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4. (Twice Amended) The semiconductor integrated circuit of claim 2,

wherein said bias adjustment circuit is configured to step down said second bias current in response to judgment that a value obtained by said successively summing is smaller than said reference value by said comparator circuit, and

wherein said control circuit is configured to cease operation thereof in response to judgment that said value obtained by said successively summing is larger than said reference value.

5. (Twice Amended) The semiconductor integrated circuit of claim 2,

wherein said bias adjustment circuit is configured to step down said second bias current in response to judgment that a value obtained by said successively summing is smaller than said reference value by said comparator circuit, and step up said second bias current in response to judgment that said value obtained by said successively summing is larger than said reference value by said comparator circuit,

wherein said control circuit is configured to cease operation thereof in a case where an absolute value of a difference between said value obtained by said successively summing and said reference value is smaller than a given value.